

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 02-05-04
Application Type:: Regular
Subject Matter:: Utility
Title:: Multi-Purpose Digital Frequency
Synthesizer Circuit for a
Programmable Logic Device
Docket Number:: X-929-2P-2P-1D US
Request for Early Pub?:: No
Request for Non-Pub?:: No
Total Drawing Sheets:: 14
Small Entity?:: No
Petition included?:: No

INVENTOR INFORMATION

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Andy
Middle Name:: T.
Family Name:: Nguyen
Street:: 775 Salt Lake Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95133

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/306,149	11-26-02
10/306,149	Cont. In Part	10/193,070	07-10-02
10/193,070	Cont. In Part	09/942,230	08-28-01

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124